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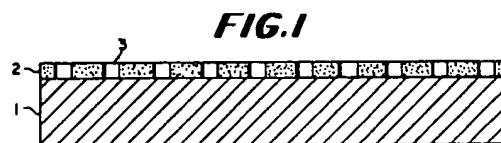
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(54) **Method for the manufacture of large single crystals.**

(57) A method is disclosed for producing large single crystals. In one embodiment, a single crystal of electronic grade diamond is produced having a thickness of approximately 100-1000 microns and an area of substantially greater than 1 cm.<sup>2</sup>, and having a high crystalline perfection which can be used in electronic, optical, mechanical and other applications. A single crystalline diamond layer is first deposited onto a master seed crystal and the resulting diamond layers can be separated from the seed crystal by physical, mechanical and chemical means. The original master seed can be restored by epitaxial growth for repetitive use as seed crystal in subsequent operations. Large master single crystal diamond seed can be generated by a combination of oriented smaller seed crystals by lateral epitaxial fusion. Since there is no limit to how many times seed combination step can be repeated, large diamond freestanding wafers comparable in size to silicon wafers can be manufactured.



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## FIELD OF THE INVENTION

This invention relates to the manufacture of large single crystals of diamond, cubic boron nitride, silicon carbide and similar crystals which are difficult to manufacture in large dimensions. More particularly, the invention relates to a chemical vapor deposition (CVD) method for producing single crystal, electronic grade diamond wafers larger than single crystal diamond wafers that are currently available, i.e. areas greater than about 1 cm<sup>2</sup>.

## BACKGROUND OF THE INVENTION

Single crystal diamond wafers are available to the electronics industry in sizes of about 1 cm<sup>2</sup> at commercially unattractive prices. There is a need in the electronics industry for diamond semiconductors because of their superior electronic properties in comparison to silicon and other currently used semiconductor materials. Single crystal diamond wafers of sizes up to about 1 cm<sup>2</sup> are commercially available and are produced by a high temperature, high pressure process; see a sales brochure entitled "Synthetic Diamond - Sumicrystal-Heat Sink for Laser and Microwave Diode", Sumitomo Electric Industries, Ltd. Severe technical difficulties associated with scaling up the crystal growing reactors have restricted the size of such diamond wafers.

Single crystal diamond wafers have not heretofore been available by other approaches such as chemical vapor deposition (CVD). However, there are reactivated CVD techniques which have been described in the prior art for approaching the goal of single crystal diamond wafers.

One technique describes the production of what is reported to be large area mosaic diamond films consisting of approximately 100 μm single diamond crystals disoriented randomly to within a few degrees of a silicon (Si) substrate crystallographical axis. Such mosaic diamond films are said to approach the quality of single diamond crystals; see N. W. Geis and H. I. Smith, *Proceedings of the Second International Symposium on Diamond Materials*, 179th Meeting of the Electrochemical Society in Washington, D.C., May 5-10, 1991, pages 605-607. In this technique, commercially available (111)-faceted diamond seeds having diameters of 75 to 100 μm are deposited on (100)-oriented Si substrates which had been patterned and etched using standard photolithographic methods to form 90 μm square etch pits on 100 μm centers faceted on (111)-planes. Homoepitaxial diamond is grown on the diamond seeds to form a continuous diamond film composed of a plurality of approximately oriented small crystals.

The disadvantages of such a technique over that of the method of the present invention are that default holes occur in the resulting film as a result of missing seed crystals in some of the etch pits and that a slight misorientation occurs among the individual single crystal grains. Therefore, this prior art technique did not produce large single crystals.

In silicon microelectronics, selected area silicon growth processes have been used to fabricate sophisticated three-dimensional devices; see R.P. Zingg, J.A. Friedrich, G.W. Neudeck, B. Hofflinger, *IEEE Trans. Electron Devices* 37, 1452(1990) and P.J. Schubert, G.W. Neudeck, *IEEE Electron Device Lett.* 11, 181(1990). In gallium arsenide microelectronics, selected-area growth has been found to localize substrate defects to the area of epitaxial growth above the original seed holes of the masking layer. Few defects are observed to propagate into the epitaxial lateral overgrowth; see D. Pribat, M. Dupuy, P. Legagneux, C. Collet, *Appl. Phys. Lett.* 57, 375 (1990).

In the emerging diamond technology, similar results were found by R.A. Rudder et al, who succeeded to deposit diamond above a photolithographically defined large electronic device area and observed isotropic overgrowth advancing vertically and horizontally by about the same rate; see R.A. Rudder, J.B. Posthill, G.C. Hudson, D. Malta, R.E. Thomas, R.J. Markunas, T.P. Humphreys, R.J. Nemanich, *Proceedings of the Second International Conference New Diamond Science and Technology*, Washington, D.C., September 23-27, 1990, page 425. The quality of homoepitaxially grown diamond above the holes in the masking layer on the device was claimed to be superior to the quality of underlying diamond substrate; see J.B. Posthill, R.A. Rudder, G.C. Hudson, D.P. Malta, G.C. Fountain, R.E. Thomas, R.J. Markunas, T.P. Humphreys, R.J. Nemanich, *Proceedings of the Second International Symposium on Diamond Materials*, Proceedings Volume 91-8 of the Electrochemical Society, May 5-10, 1991, Washington, D.C., page 274; G.S. Gildenblat, S.A. Grot and A.R. Badzian, *The Electrical Properties and Device Applications of Homoepitaxial and Polycrystalline Diamond Films*, IEEE (1991); J.L. Davidson, C. Ellis, and R. Ramesham, *J. Electron Mat.*, Vol. 18, page 711 (1989); S.A. Grot, C.W. Hatfield, G.S. Gildenblat, A.R. Badzian and T. Badzian, *The Electrical Properties of Selectively Grown Homoepitaxial Diamond Films*, *Appl. Phys. Lett.*, Vol. 58, pages 1542-1544 (1991); and R. Ramesham, T. Roppel, B.F. Hajek, C. Ellis, B.H. Loo, "Selective Growth of Boron-Doped Polycrystalline Diamond Thin Films", *Proceed. Second Int. Conf. on New Diamond Science and Technology*, Washington D.C. Sept 23-27, 1990.

The superior quality of a laterally propagated epitaxial layer is believed to be due to the so called "necking effect", which is frequently used in Bridgman or Czochralski crystal growth. Necking down the growing crystal limits the propagation of dislocation from the seed crystal only to the straight direction of growth, but not in lateral directions. In the references mentioned above, R.A. Rudder and J.B. Posthill have demonstrated that the same effect of lateral overgrowth, which is being used successfully in silicon and gallium arsenide microelectronics for the fabrication of three dimensional integrated circuits, will allow for the fabrication of three dimensional integrated circuits in diamond microelectronics technology. There is no suggestion in these references to use this technology to grow large monocrystalline diamond plates.

Another technique called SENTAXY, originally used in silicon technology, was utilized by H. Yagyu et al. This technique was used to generate diamond polycrystalline growth consisting of a regular array of relatively large diamond single crystal (10 $\mu$ ) randomly or preferentially crystallographically oriented in relation to the silicon surface. A polycrystalline array of diamonds was grown by a microwave-assisted chemical vapor deposition technique; see H. Yagyu, J.S. Ma, H. Kwarada, T. Yonehara, A. Hiraki, *Proceedings of the Second International Symposium on Diamond Materials*, Proceedings Volume 91-8 of the Electrochemical Society, May 5-10, 1991, Washington, D.C., page 274. This technique can be used to elucidate nucleation and crystal growth of diamond, because the technique enables one to grow crystals separately and to control nucleation sites artificially thereby facilitating the study of diamond nucleation sites on the substrate. The technique also enables the studying of initial growth of CVD diamond ex-situ by increasing the size of the same particle by repetitive successive growth and observing the same via a scanning electron microscope (SEM). However, this reference makes no suggestion of manufacturing large diamond single crystals.

Single crystal diamond films having very small surface areas that are comparable to that of natural diamond crystals, have been grown on natural and high temperature/high pressure diamond substrates; see A. R. Badzian, T. Badzian and L. Pullione, "Superhard Materials and Superhard Coatings", *Int. J. Refractory Metals and Hard Materials*, Vol. 9, pages 92-95 (1990). Several problems were noted as being unsolved in connection with the growing of such crystals; see the G. S. Gildenblat et al reference referred to above. One of the specific problems mentioned in this paper was that of depositing diamond via CVD onto non-diamond substrates to produce larger heteroepitaxial films, which were reported to be vital to elec-

tronic applications. The method of the present invention overcomes the overall problem referred to in this paper because Applicants' method enables one to produce large single crystal diamonds, i.e. having areas of greater than 1 cm<sup>2</sup>, which materials are vital to the electronics industry.

#### SUMMARY OF THE INVENTION

In contrast to the techniques disclosed in the prior art, the method of the present invention solves the problem of generating large single crystals with high crystalline perfection which can be used in electronic, optical, mechanical and other applications.

According to the initial steps of this method, a plurality of substrate surfaces or seed wafers are oriented to form a seed plate which is patterned with a plurality of holes spaced apart to form a masking layer on the seed plate. The patterned seed plate is placed into a crystal growing reactor and epitaxial crystals are grown through the seed holes and then over the masking layer to form a continuous layer of monocrystalline material over the entire surface of the masking layer for a period of time to achieve a thickness in the range of about 1  $\mu$ m to about 3,000  $\mu$ m. In the case of freestanding crystal wafers, a thickness in the range of about 100  $\mu$ m to about 2,000  $\mu$ m is preferred.

The resultant fully grown epitaxial crystal is separated from the seed plate by physical, mechanical or chemical means. For example, the masking layer is etched or burned out and then the pillars of crystal between the seed plate and the epitaxially grown crystal are cleaved or eliminated by mechanical, physical, cutting or chemical means. The single crystal recovered from this process has an area that is greater than each of the plurality of seed wafers that comprise the seed plate.

After the initial steps of the method have been carried out, the resulting single crystal can then be used as the seed plate in place of the plurality of smaller seed wafers used in the initial steps of the method and the remaining step of crystal growing, separation and recovery are repeated.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Further features and advantages of the method disclosed herein will become apparent from the following and more particular description of the preferred embodiment of the invention as illustrated in the accompanying drawings, in which:

FIGURES 1-6 are schematic illustrations (not to scale) of the step-by-step fabrication of a diamond single crystal;

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**FIGURE 2A** is an enlargement of one of the holes in the masking layer during the initial period of diamond crystal growth shown in **FIGURE 2**;

**FIGURE 3A** is a top plan view of one corner of the masked seed plate 1, as schematically illustrated in **FIGURE 3**, showing the regular diamond crystal growth with octagonal cross-section before the crystals become fused into a single crystal;

**FIGURE 3B** is an oblique pictorial view of an individual diamond nucleus that forms above the holes of the masked layer as schematically illustrated in **FIGURE 3**; and

**FIGURES 7-8** are schematic illustrations (not to scale) of two of the essential steps in the fabrication of a large single diamond crystal according to the method of the present invention.

#### DETAILED DESCRIPTION OF THE INVENTION

As shown in **FIGURE 1**, substrate 1 is shown with masking layer 2 which is patterned with a plurality of evenly distributed holes 3 made by standard photolithographical procedures. Suitable masking layer materials can include any material which is chemically compatible with the substrate such as metals, glassy materials, polymers, ceramics and pyrolyzed organic compounds. Specific examples of such materials include, but not are limited to the following: silicon, tungsten, molybdenum, nickel, gold, copper, soft carbon, carbon soot, diamond-like carbon, hydrogenated diamond-like carbon, graphite,  $\text{SiO}_2$ ,  $\text{SiC}$ ,  $\text{Si}_3\text{N}_4$ ,  $\text{MgO}$ ,  $\text{CaO}$ , polycarbonate, polypropylene, polyethylene, and photoresist compounds.

The patterned substrate is placed into a suitable crystal growing reactor. During approximately the first hour of growth, an initial level 5 of epitaxial diamond layer is reached within the confines of hole 3, as shown in **FIGURE 2**. During a period of 30 minutes to 1 hour of the growth process, depending on the specific geometries involved, the epitaxial diamond layers within holes 3 increase from level 5 through levels 10 to level 12 shown in **FIGURE 2A**. As the growth process continues, the epitaxial diamond crystal 14 laterally and vertically grows out of holes 3 and begins to over-spread the surface of masking layer 2 as shown in **FIGURES 3, 3A** and **3B**. Specifically, in the case of diamond growth through holes 3 of a masking layer 2 comprising  $\text{SiO}_2$  on (100) diamond substrate, one of the individual diamond crystals is illustrated by **FIGURE 3B** in which each of the facets is identified. **FIGURE 3B** shows that the individual crystals are limited by (100) and (111) crystallographical faces of high perfection during the first several hours of growth.

After the small diamond nuclei growing through holes 3 coalesce into a single crystal diamond layer 20 and the diamond layer reaches desired thickness for the application (2 microns to 1 mm, or more), as shown in **FIGURE 4**, the growth process is stopped and single crystal diamond layer 20 is separated from the diamond seed plate 1 into a freestanding diamond wafer by mechanical, chemical, or other means. For example, separation can be accomplished by etching, laser cutting, cleaving, thermal shock in a temperature gradient, mechanical forces induced by different thermal expansion of the masking layer and diamond and other similar methods. In the case of a  $\text{SiO}_2$  masking layer, the entire composite structure illustrated in **FIGURE 4** is immersed in an etching bath such as hydrofluoric acid to leave diamond crystal pillars 22 which occupy the space of holes 3. Pillars 22 are etched through to form crystal 20 containing the upper protrusions 23 and the original seed plate 1 containing the lower protrusions 24. Upper and lower protrusions 23 and 24, that remain after the etching of pillars 22, are removed by mechanical or chemical means. For example, crystal 20 and seed plate 1 can be heated in an air atmosphere at  $750^\circ\text{C}$  to etch away the upper and lower protrusions.

The original diamond seed plates will be reused in a new cycle of the same process for manufacturing another diamond single-crystalline freestanding wafer. Large diamond seed wafers are generated by precise alignment of several small crystallographically oriented diamond seed wafers to form larger seed plates and subsequent diamond growth through holes in the mask until the desired size of diamond free-standing wafer is achieved. Methods for achieving precise alignment of the seed wafers are well known in the art. **FIGURE 7** illustrates the assembly of four of these single diamond crystals, 20, 26, 27 and 28, to form square seed plate 29. However, the exact number of separate seed wafers that are operably positioned to form the seed plate comprising the plurality of seed wafers is not critical and will depend on the particular size of the final crystal product. Masking layer on seed plate 29 as shown in **FIGURE 8** is patterned masked by photolithographic procedures in the same manner as the individual seed wafers and seed plate 29 containing the patterned masking layer 30 is placed in a crystal growing reactor to grow large epitaxial diamond crystal 32 on seed plate 29 to form composite 34. Crystal 32 can grow to sufficient thickness to be freestanding or to a thin crystal epitaxial layer which can be mounted in a supporting package or frame or as a layer in a composite material. Such a package or frame for the thin crystal can be of any suitable metal, ceramic or plastic. For ease of

separating thin crystal layer 32 from seed plate 29, a layer of copper or other suitable material can be deposited over the surface of the layer 32.

In one embodiment in which the masking layer comprises soft carbon, soot, pyrolyzed polymers and similar carbonaceous materials, the epitaxially grown crystal layer is separated from the seed plate by etching the masking layer at temperatures in the range of about 250° to about 600° C in the presence of an oxygen-containing atmosphere and raising the temperature in the range of about 600° to about 900° C to etch away the diamond pillars in an oxidizing atmosphere.

The commercially important feature of the present invention is that after the large, epitaxially grown crystal layer is separated from said seed plate, the resulting separated large crystal is used in the production of additional single crystals. Once an inventory has been manufactured of seed plates of varying lengths and widths ranging in area from about 2 to about 20 cm.<sup>2</sup>, the large area seed plates are patterned masked and placed directly into the crystal growing reactor for replication of single crystals of substantially the same area.

The method of the present invention produces large crystals using small crystal starting materials, i.e. the seed plate, such as natural and high pressure/high temperature synthetic diamond crystals which are available in sizes up to about 1 cm.<sup>2</sup>. This is also applicable to other technically important crystals which are available only in small crystal sizes which can be used as the seed crystals for the present method. Such materials include silicon carbide and cubic boron nitride and similar crystals which are difficult to manufacture in large dimensions.

Diamond growth for the purpose of this invention can be accomplished by any technique capable of growing diamond epitaxial layers, which is not detrimental to the masking layer. The exact crystal growing techniques that are used to grow the large crystals by the present method are not critical. The following list of techniques is illustrative, but not exhaustive: Hot filament CVD (HFCVD), microwave-assisted CVD, radio frequency plasma CVD (RFCVD), DC plasma assisted CVD, electron assisted CVD, Electron Cyclotron Resonance (ECR) plasma CVD, DC or AC arc plasma jet, and combustion flame diamond growth deposition. The following references provide an illustration of the state of the art of such techniques: U.S. Patent Numbers 4,767,608; 4,740,263; 4,734,339 and 4,707,384.

One of the most frequently used chemical compounds for diamond growth is methane (0.1-7% vol.) with hydrogen gas making up the balance. Methane can be replaced with a variety of other compounds containing carbon, hydrogen, halogen,

and sometimes oxygen. The following hydrocarbons have been successfully used: methane, ethane, propane, acetylene, ethene and benzene. The use of organic compounds such as methyl alcohol, ethyl alcohol, acetone, acetic acid results typically in faster diamond growth rates. Halogens or halocarbons based mixtures with hydrocarbons or alcohols and small oxygen addition allow growth of diamond crystal at lower temperatures. A carbon monoxide mixture (e.g. 15% by volume) with hydrogen is also known to result in good quality diamond growth. Oxygen and water are sometimes added to carbon-containing mixtures in order to modify character of crystal growth. Inert gases are also sometimes added to reaction mixture.

The purity of gases is critical for electronic applications in respect to species that become incorporated in the diamond crystal and are either electrically active or result in formation of inclusions. An example of such undesirable impurity is nitrogen. On the other hand, electrically active impurities are sometimes deliberately added in order to render the diamond crystal electrically conductive. Examples of such impurities added to growth atmosphere are diborane or some other boron compounds, which make diamond a p-type conductor.

The temperature of diamond substrate during the growth can be chosen from 350° C to 1100° C. The preferred temperature range for HFCVD with 1% methane and 99% hydrogen is 600°-1050° C at 30 torr pressure in the reactor. The gas mixture pressure depends on the particular technique used. Typical pressure for the HFCVD technique is between 20-40 torr. The range of gas pressure is from low pressures, i.e. 0.5 torr which is sometimes used in RFCVD, to 1 atm. in the DC arc jet technique. The flow rate of the gas mixture depends on specific crystal growing technique used and the size of the crystal growing reactor. Gas flow rates for a 5 cm diameter HFCVD reactor range from 1 cm<sup>3</sup>/min. to 100 cm<sup>3</sup>/min., preferably in the range 5 cm<sup>3</sup>/min. to 15 cm<sup>3</sup>/min.

The masking layer can be deposited on diamond seed by a number of techniques routinely used in semiconductor industry such as evaporation, sputtering, CVD and the like. The material chosen for the masking layer should be compatible with diamond and the crystal growing process at crystal growth temperatures. It is preferable to choose material for the masking layer which after the deposition is very smooth or amorphous, because smooth or amorphous surfaces slow down parasitic diamond nucleation thereon. This allows for diamond nuclei growing on diamond substrate through patterned holes to coalesce into a continuous single crystal diamond crystal epitaxial layer before parasitic nuclei on the surface of masking

layer reach significant dimensions. Small randomly oriented parasitic spontaneously nucleated diamond nuclei get engulfed under the growing diamond epitaxial layer and do not interfere with the epitaxial diamond layer itself.

Patterning of the masking layer can be done by standard photolithographical techniques routinely used in semiconductor industry for device manufacturing. A photolithographical mask is designed which allows for printing a design on photoresist, spinned off on masking layer on the top of diamond seed. The design consists of circles or squares or other geometrical figures with dimensions from 0.1  $\mu\text{m}$  - 4  $\mu\text{m}$  separated by distance of 0.5-20.0  $\mu\text{m}$  on rectangular, hexagonal or other grit pattern. After developing the exposed photoresist, holes are etched in masking layer. The thickness of masking layer determines the depth of etched holes. The thickness of masking layer can be from 0.01  $\mu\text{m}$  to 5  $\mu\text{m}$  with preferred range from 0.1  $\mu\text{m}$  - 2  $\mu\text{m}$ . The surface of the masking layer can also be rendered amorphous by ion bombardment, i.e. with argon ions. This treatment further inhibits undesirable spontaneous diamond nucleation on the surface of masking layer.

In the preferred embodiment of the present invention, HFCVD and microwave-assisted CVD reactors have been used to manufacture large single diamond crystals. A general description of the type of HFCVD reactors and the process conditions suitable for depositing such diamond crystals via HFCVD are set forth in patent application, Serial Number 578,637, filed by the assignee of the present invention on 6 September 1990, the pertinent portions of which are incorporated by reference herein. A general description of the type of microwave-assisted CVD reactors and the process conditions suitable for depositing such diamond crystals via microwave-assisted CVD are set forth in A. R. Badzian, T. Badzian, R. Roy, R. Messier, K. E. Spear, "Crystallization of Diamond Crystals and Films By Microwave-Assisted CVD [II]", Mat. Res. Bulletin, Vol. 23, pages 531-548 (1988), the pertinent portions of which are incorporated by reference herein.

The examples which follow illustrate the method of the invention and the single crystal diamond wafers having large areas that are produced thereby.

#### EXAMPLE 1

A natural IIA type diamond single crystal square substrate of (100) orientation and 1 mm x 1 mm x 0.25 mm dimensions was used as a seed plate for crystal growth. The diamond seed plate was cleaned with organic solvents by rinsing for 2 min. in trichloroethane, 2 min. in acetone, 2 min. in

ethyl alcohol and 5 min. in deionized water, and blown dry with nitrogen gas. Next, the wafer was placed in an ion beam sputtering machine in order to deposit on its polished surface, 0.5  $\mu\text{m}$  of silicon dioxide. After evacuating to a pressure of  $5 \times 10^{-6}$  torr, the diamond substrate was precleaned with an argon ion beam generated from an 11 cm diameter Kaufman source at 500 eV and 137 mA for 1 min.

Following the precleaning step, 0.5  $\mu\text{m}$  of silicon dioxide was deposited from a silicon dioxide source bombarded with an argon ion beam generated from a 5 cm diameter Kaufman source at 1000 eV and 100 mA at  $1 \times 10^{-4}$  torr pressure. The deposition rate of silicon dioxide was 100  $\text{\AA}/\text{min}$ . This 0.5  $\mu\text{m}$  silicon dioxide layer was used as a masking layer in which 2  $\mu\text{m}$  diameter holes on 4  $\mu\text{m}$  centers on a rectangular grid were opened by the following photolithographical procedure. After spinning the positive photoresist at 5,000 rpm for 20 sec. and a 90°C bake, the photoresist layer on the top of the substrate was exposed in the mask aligner with the appropriate mask for 20 sec. and developed for 40 sec. in developer. This step opened the holes in photoresist. Holes in the silicon dioxide masking layer were etched by buffered hydrofluoric acid until the diamond surface was reached. Photoresist stripper at 70°C for 10 min. was used to remove the remainder of the photoresist, followed by a three times repeated rinse procedure of acetone, methanol and deionized water.

The resulting patterned diamond substrate was dried in an oven at 120°C for 15 min. and was then placed on a wafer holder in an HFCVD reactor for diamond epitaxial growth. The details of the HFCVD reactor used in this example are described in U.S. Serial Number 578,734 referred to above. After the distance between carburized tantalum filament and patterned diamond seed plate had been adjusted to about 10 mm, the HFCVD reactor was evacuated three times and was refilled with helium. Hydrogen (99.999% purity) was then admitted into the reactor at a flow rate of 20 sccm/min. and the pressure was adjusted to 30 torr. The temperature of the patterned diamond substrate was raised to 850°C at a rate of 100°C/min. A fraction of the molecular hydrogen was decomposed by the hot filament into atomic hydrogen which etches the diamond surface. This caused the elimination of any polishing damage on the surface of the diamond substrate and provided for hydrogen termination of the diamond surface. After the annealing in a hydrogen atmosphere had been carried out for 20 min., diamond epitaxial growth on the diamond substrate was initiated at 850°C using a 30 torr mixture of 1% methane and 99% hydrogen at the same 20 sccm/min. flow rate used for annealing. The temperature of the hot filament was main-

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tained at about 2300°C throughout the hydrogen anneal step and the diamond epitaxial growth. Epitaxial growth of diamond was carried out for 48 hours at a diamond growth rate of about 0.83μm/hour. At the termination of growth cycle, helium gas was admitted to the HFCVD reactor at 30 torr and a flow rate of 10 sccm/min. The resulting diamond crystal on the diamond substrate composite was cooled at a rate of 100°C/min. to room temperature. The increase in the thickness of patterned diamond substrate was measured to be 40 μm, which was due to the newly grown diamond epitaxial layer above the silicon dioxide patterned masking layer.

After the termination of the crystal growth cycle, the composite is immersed in buffered hydrofluoric acid for a sufficient period of time to dissolve the silicon dioxide masking layer with the help of ultrasonic agitation. The newly grown 40 μm thick diamond epitaxial layer is then separated from the diamond substrate by etching in an oxidizing atmosphere. Specifically, the composite is subjected to air at 750°C for a sufficient period of time to etch the 2 μm diamond pillars connecting the newly grown single crystal diamond layer with the original diamond substrate until separation occurs.

#### EXAMPLE 2

A natural 2A type diamond single crystal square substrate of (100) orientation and 1mm x 1mm x 0.25mm dimensions was used as a seed plate for crystal growth. The diamond seed plate was cleaned with organic solvents by rinsing for 2 min. in trichloroethane, 2 min. in acetone, 2 min. in ethyl alcohol and 5 min. in deionized water, and blown dry with nitrogen gas. Next, the wafer was placed in an ion beam sputtering machine in order to deposit on its polished surface 0.5μm of silicon dioxide. After pump down to  $5 \times 10^{-6}$  torr, the diamond substrate was precleaned with an ion beam generated from 11cm diameter Kaufman source at 500eV and 137mA for 1 min.

Following the preclean, 0.5μm of silicon dioxide was deposited from a silicon dioxide source bombarded with an argon ion beam generated from a 5cm diameter Kaufman source at 1000eV and 100mA at  $1 \times 10^{-4}$  torr pressure. The deposition rate of silicon dioxide was 100Å/min. This 0.5μm silicon dioxide layer was used as a masking layer in which 2μm diameter holes on 4μm centers on a rectangular grid were opened by the following photolithographical procedure. After spinning the positive photoresist at 5,000rpm for 20 sec. and a 90° bake, the photoresist layer on the top of the substrate was exposed in the mask aligner with the appropriate mask for 20 sec. and developed for 40 sec. in developer. This step opened the holes in

the photoresist and the openings in the silicon dioxide were etched by buffered hydrofluoric acid until the diamond surface was reached. Photoresist stripper at 70°C for 10 min. was used to remove the remainder of the photoresist, followed by a three step rinse procedure of acetone, methanol and deionized water.

The patterned diamond substrate was dried in an oven at 120°C for 15 min. and was placed on a wafer holder in a microwave plasma assisted chemical vapor deposition reactor for diamond epitaxial growth as described in an article by A.R. Badzian, T. Badzian, R. Roy, R. Messier, K.E. Spear in Materials Research Bulletin, Vol. 23, p.p. 531-548, 1988. After evacuation of the microwave plasma assisted chemical vapor deposition reactor, 30 torr of 99.999% pure hydrogen was admitted to the reactor at a flow rate of 100 sccm/min. and the temperature of the diamond substrate was raised to 880°C in 10 min. This hydrogen treatment cleaned, etched and conditioned the surface of the diamond substrate for subsequent epitaxial growth which was performed during the following 30 min. in the 30 torr atmosphere of a mixture of 2 volume % of methane in 98 volume % of hydrogen at the flow of 100 sccm. After this initial nucleation and growth period the concentration of methane was decreased to 1 volume% in 99 volume% hydrogen at 30 torr and a flow rate of 100 sccm/min. Temperature was kept constant at 880°C during 12 hours. After this 12 hour period of epitaxial growth the diamond substrate was cooled to room temperature in the same mixture of gases during 20 min. The reactor was then evacuated and air atmosphere admitted to the reactor. When the thickness of the sample was measured, an increase of 12μm was observed, which corresponded to approximately 1μm/hour diamond epitaxial growth rate.

After terminating the crystal growth cycle, the silicon dioxide masking layer is dissolved in buffered hydrofluoric acid with the help of ultrasonic agitation. The newly grown 12μm thick diamond epitaxial layer is separated from the diamond substrate by etching away in air at 750°C, the 2 micron diamond pillars connecting the newly grown single crystal diamond layer with original diamond substrate.

#### EXAMPLE 3

A 0.5μm thick layer of silicon dioxide is deposited by sputtering on four 1cm<sup>2</sup> single-crystal diamond seed plates of (100) orientation in the manner set forth under Example 1. Next, 0.5μm round holes located in a rectangular grid with 2μm centers are opened by a photolithographic technique with a suitable mask in the silicon dioxide layer

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until the diamond surface is reached. Four 1cm<sup>2</sup> patterned diamond single crystal wafer of (100) orientation are precisely aligned in the reactor wafer holder by methods known in the art to form seed plate 29 as indicated in FIGURES 7 and 8. The same HFCVD technique used in Example 1 is used to grow the epitaxial layer. The epitaxial growth of diamond is carried out until the thickness of a new diamond layer above the masking layer reaches the desired thickness of 500μm. After termination of the crystal growth cycle, the SiO<sub>2</sub> masking layer is dissolved in hydrofluoric acid with the help of ultrasonic agitation. The newly grown 500μm thick epitaxial layer is separated from the seed plate by the procedures set forth under Example 1. This epitaxial layer after the termination of the etching step becomes a freestanding monolithic single crystal diamond wafer (20mm x 20mm x 0.5mm), four times larger than each of the four 1cm<sup>2</sup> segments that is used as a combined diamond seed plate at the beginning of the process. The original diamond seeds can be repolished and regrown to their original thickness by the same CVD epitaxial process without a masking step before being reused in the next growth cycle.

After generating four 20mm x 20mm x 0.5mm single crystal diamond wafers, the epitaxial growth process is repeated as described above with the newly generated four diamond seeds. This step again results in a quadrupling of the wafer area to the size of 40mm x 40mm x 0.5 mm. The step is then repeated until the final desirable area of freestanding diamond wafer is achieved. Subsequent epitaxial plates are then produced from the large area diamond wafer by the same process.

#### EXAMPLE 4

A 0.5μm thick layer of gold is deposited by evaporation on four 1cm<sup>2</sup> single-crystal diamond seed plates of (111) orientation. Next, 0.5μm round holes located in a rectangular grid with 2μm centers are opened in the gold layer by a photolithographic technique with a suitable mask and etched until the diamond surface is reached. Four 1cm<sup>2</sup> patterned diamond single crystal plates of (111) orientation are precisely aligned in the reactor wafer holder as indicated in Fig. 7 and 8. The hot filament CVD technique is used to grow epitaxial layer. The temperature of the seed crystals is raised to 900°C in the 30 torr hydrogen atmosphere. A fraction of the molecular hydrogen is decomposed by a hot filament into atomic hydrogen. A hydrogen atmosphere is used to clean, etch and condition the surface of diamond seed plates for 20 minutes before subsequent diamond epitaxial growth at 900°C in the 30 torr mixture of 1% CH<sub>4</sub>, and 99% hydrogen. The 2300°C hot

filament is 10mm distant from the diamond wafers during the process. Epitaxial growth of diamond is carried out until the thickness of the new diamond layer above the masking layer reaches the desirable value of 1000μ. After termination of the crystal growth cycle, the gold masking layer is dissolved in aqua regia with the help of ultrasonic agitation. The newly grown 1000μ thick epitaxial layer is separated from the seed crystals by cleaving. This cleaving step severs the 0.5μm diameter diamond pillars connecting the original diamond seed with the newly grown diamond epitaxial layer.

This epitaxial layer after the cleaving step becomes a freestanding monolithic single crystal diamond wafer (20mm x 20mm x 1.0mm), four times larger than each of the four 1cm<sup>2</sup> segments used as a combined diamond seed at the beginning of the process. Original diamond seeds are then repolished and regrown to their original thickness by the same hot filament CVD epitaxial process without a masking step before being used in the next growth cycle.

After generating four 20mm x 20mm x 1.0mm single crystal diamond wafers, the epitaxial growth process is repeated as described above with the newly generated four diamond seeds. This step results again in quadrupling of the wafer area to the size of 40mm x 40mm x 1.0mm. The step is repeated until the desirable area of freestanding diamond wafer is achieved. Subsequent epitaxial plates are then produced from the large area diamond wafer by the same process.

#### EXAMPLE 5

A 1.0μm thick layer of positive photoresist is deposited by spinning on four 1cm<sup>2</sup> single-crystal diamond seed plates of (100) orientation. Next 0.5μm round holes located in a rectangular grid with 2μm centers are opened by a photolithographic technique with a suitable mask in the positive photoresist until the diamond surface is reached. Four 1cm<sup>2</sup> patterned diamond single crystal plates of (100) orientation are precisely aligned in the reactor wafer holder as indicated in Fig. 7 and 8. The hot filament CVD technique is used to grow epitaxial diamond layer. Photoresist layer is pyrolyzed at 450°C for 30 min. in 30 torr hydrogen atmosphere. A fraction of the molecular hydrogen is decomposed by a hot filament into atomic hydrogen. Hydrogen atmosphere is used to clean, etch and condition the surface of the diamond for 20 minutes before subsequent diamond epitaxial growth at 680°C in the 30 torr mixture of 1% CH<sub>4</sub> and 99% hydrogen. The 2300°C hot filament is 10mm distant from the diamond wafers during the process. Epitaxial growth of diamond is carried out until the thickness of the new diamond layer above



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the masking layer reaches the desirable value of  $300\mu$ . After termination of the crystal growth cycle, carbonized photoresist is oxidized in oxygen at  $550^\circ\text{C}$ . The newly grown  $300\mu$  thick epitaxial layer is separated from seed crystals by etching away the half micron of diamond in air atmosphere at  $750^\circ\text{C}$ . This oxidizing step eliminates the  $0.5\mu$  diameter diamond pillars connecting the original diamond seed with the newly grown diamond epitaxial layer.

This epitaxial layer after the termination of the oxidizing step becomes a freestanding monolithic single crystal diamond wafer ( $20\text{mm} \times 20\text{mm} \times 0.3\text{mm}$ ), four times larger than each of the four  $1\text{cm}^2$  segments used as combined diamond seed at the beginning of the process. Original diamond seeds are then repolished and regrown to their original thickness by the same CVD epitaxial process without masking step before being used in the next growth cycle.

After generating four  $20\text{mm} \times 20\text{mm} \times 0.3\text{mm}$  single crystal diamond wafers, the epitaxial growth process is repeated as described above with the newly generated four diamond seeds. This step results again in quadrupling of the wafer area to the size of  $40\text{mm} \times 40\text{mm} \times 0.5\text{mm}$ . The step is repeated until the desirable area of freestanding diamond wafer is achieved. Subsequent epitaxial diamond plates are then produced from the large area diamond wafer by the same process.

#### EXAMPLE 6

A  $0.5\mu\text{m}$  thick layer of silicon dioxide is deposited by sputtering on four  $1\text{cm}^2$  single-crystal diamond seed plates of (111) orientation in the manner set forth under example 1. Next,  $0.5\mu\text{m}$  round holes located in a rectangular grid with  $2\mu\text{m}$  centers are opened by a photolithographic technique with a suitable mask in the silicon dioxide layer until the diamond surface is reached. Four  $1\text{cm}^2$  patterned diamond single crystal plates of (111) orientation are precisely aligned in the reactor wafer holder to form seed plate 29 as indicated in Figures 7 and 8.

The hot filament CVD technique is used to grow epitaxial layer. The temperature of the seed crystals is raised to  $900^\circ\text{C}$  in the 30 torr hydrogen atmosphere. A fraction of the molecular hydrogen is decomposed by hot filament into atomic hydrogen. Hydrogen atmosphere is used to clean, etch and condition the surface of diamond seed plates for 20 minutes before subsequent diamond epitaxial growth at  $900^\circ\text{C}$  in the 30 torr mixture of 1%  $\text{CH}_4$ , and 99% hydrogen. The  $2300^\circ\text{C}$  hot filament is 10mm distant from the diamond wafers during the process. Epitaxial growth of diamond is carried out until the thickness of new diamond

layer above the masking layer reaches the desirable value of  $500\mu\text{m}$ .

After termination of the crystal growth cycle, silicon dioxide masking layer is dissolved in buffered hydrofluoric acid. Then, the new diamond composite is mounted by soldering on a water chilled copper block. The newly grown diamond layer,  $500\mu$  thick, is rapidly heated with an acetylene torch to about  $1000^\circ\text{C}$ . Thermal expansion of the newly grown diamond layer causes mechanical stresses in the diamond pillars which severs the  $0.5\mu\text{m}$  pillars along the (111) easy cleavage plane.

This epitaxial layer after the termination of the separation step becomes a freestanding monolithic single crystal diamond wafer ( $20\text{mm} \times 20\text{mm} \times 0.5\text{mm}$ ), four times larger than each of the four  $1\text{cm}^2$  segments used as combined diamond seed at the beginning of the process. Original diamond seeds are then repolished and regrown to their original thickness by the same CVD epitaxial process without masking step before being used in the next growth cycle.

After generating four  $20\text{mm} \times 20\text{mm} \times 0.5\text{mm}$  single crystal diamond wafers, the epitaxial growth process is repeated as described above with the newly generated four diamond seeds. This step results again in quadrupling of the wafer area to the size of  $40\text{mm} \times 40\text{mm} \times 0.5\text{mm}$ . The step is repeated until the desirable area of freestanding diamond wafer is achieved. Subsequent epitaxial plates are then produced from the large area diamond wafer by the same process.

#### EXAMPLE 7

Four natural II.A type diamond single crystal square substrates of (100) orientation and  $10\text{mm} \times 10\text{mm} \times 0.25\text{mm}$  dimensions are used as seed plates for crystal growth. The diamond seed plates are cleaned with organic solvents by rinsing for 2 min. in trichloroethane, 2 min. in acetone, 2 min. in ethyl alcohol and 5 min. in deionized water, and blown dry with nitrogen gas. Next, the wafers are placed in an ion beam sputtering machine in order to deposit on its polished surface,  $0.3\mu\text{m}$  of hydrogenated diamond-like carbon and  $0.1\mu\text{m}$  of silicon dioxide. After evacuating to a pressure of  $5 \times 10^{-6}$  torr, the diamond substrates are precleaned with an argon ion beam generated from an 11cm diameter Kaufman source at 500 eV and 137 mA for 1 min.

Following the precleaning step,  $0.3\mu\text{m}$  of hydrogenated diamond-like carbon is deposited from a Kaufman 11cm diameter source operated on methane at ion energy 40 eV and 100 mA current at  $8 \times 10^{-5}$  torr. Following the  $0.3\mu\text{m}$  of hydrogenated diamond-like coating,  $0.1\mu\text{m}$  of silicon dioxide is deposited from a silicon dioxide source bom-

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barded with Argon ion beam generated from a 5cm diameter Kaufman source at 1000 eV and 100 mA at  $1 \times 10^{-4}$  torr pressure. The deposition rate of silicon dioxide is 100Å/min. This 0.1µm silicon dioxide layer is used as a masking layer in which 2µm diameter holes on 4µm centers on a rectangular grid are opened by the following photolithographical procedure. After spinning the positive photoresist at 5,000 rpm for 20 sec. and 90°C bake, the photoresist layer on the top of the substrate is exposed in the mask aligner with the appropriate mask for 20 sec. and developed for 40 sec. in developer. This step opens the holes in the photoresist. Holes in the silicon dioxide masking layer are etched by buffered hydrofluoric acid until the hydrogenated diamond-like surface is reached. Photoresist stripper at 70°C for 10 min. is used to remove the remainder of the photoresist, followed by a three times repeated rinse procedure of acetone, methanol and deionized water.

The resulting patterned diamond substrate is dried in oven at 120°C for 15 min. and the holes in hydrogenated diamond-like coating are opened by oxygen plasma etch until the surface of diamond is reached.

Four 1cm<sup>2</sup> patterned diamond single crystal wafers of (100) orientation are precisely aligned in the reactor wafer holder to form seed plate 29 as indicated in Figures 7 and 8. The same microwave plasma assisted CVD technique is used for diamond epitaxial growth in the manner set forth in the example 2.

Epitaxial growth of diamond is carried out for 500 hours at the rate of 1µ/hour. After termination of the crystal growth cycle, hydrogenated diamond-like carbon layer is oxidized in oxygen at 590°C. The newly grown 500µ thick epitaxial layer is separated from seed crystal by etching away 3µm of diamond in air atmosphere at 750°C. This etching step eliminates the 2µm diameter diamond pillars connecting original diamond epitaxial layer. The silicon dioxide layer is etched away by hydrofluoric acid and a freestanding diamond single crystal wafer is produced with dimensions about 20mm x 20mm x 0.5mm.

Various other embodiments and aspects of our invention will occur to those skilled in the art without departing from the spirit or scope of the invention. Having thus described the present invention, what is now deemed appropriate for Letter Patent is set forth in the following appended claims.

#### Claims

1. A method for producing a large single crystal comprising:
  - (a) depositing onto a seed plate, comprising a plurality of oriented seed wafers, a mask-

ing layer including a plurality of seed holes spaced apart in the masking layer;

(b) placing said seed plate into a crystal growing reactor;

(c) growing epitaxial crystals through said seed holes and over the masking layer to form a continuous layer of monocrystalline material over said masking layer;

(d) separating the resulting epitaxially grown layer from said seed plate; and

(e) recovering a single crystal having an area greater than the area of each of the individual seed wafers.

2. The method of Claim 1 wherein said single crystal is used as said seed plate in place of the plurality of oriented seed wafers.

3. The method of Claim 1 wherein said single crystal comprises diamond.

4. The method of Claim 1 wherein said single crystal comprises boron nitride.

5. The method of Claim 1 wherein said single crystal comprises silicon carbide.

6. The method of Claim 1 wherein said seed wafer comprises diamond.

7. The method of Claim 1 wherein said seed wafer comprises boron nitride.

8. The method of Claim 1 where said seed wafer comprises silicon carbide.

9. The method of Claim 1 wherein the thickness of said epitaxially grown crystal layer is in the range of about 1 µm to about 3000 µm.

10. The method of Claim 3 wherein said epitaxially grown crystal layer is chemical vapor deposited at temperatures in the range of about 350° to about 1100°C.

11. The method of Claim 3 wherein said epitaxially grown crystal layer is chemical vapor deposited at temperatures in the range of about 600° to about 1050°C.

12. The method of Claim 1 wherein the composition of said masking layer comprises a material selected from the group consisting of silicon, tungsten, molybdenum, nickel, gold, copper, soft carbon, carbon soot, diamond-like carbon, hydrogenated diamond-like carbon, graphite, SiO<sub>2</sub>, SiC, Si<sub>3</sub>N<sub>4</sub>, MgO, CaO, polycarbonate, polypropylene, polyethylene, photoresist com-

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pounds and mixtures thereof.

13. The method of Claim 1 wherein the composition of said masking layer comprises silicon dioxide. 5
14. The method of Claim 1 wherein the epitaxially grown crystal layer is separated from said seed plate and the resulting separated seed plate is reused in the production of large single crystals. 10
15. The method of Claim 14 wherein said seed plate is repolished and restored to its original thickness by epitaxial growth prior to being reused. 15
16. A chemical vapor deposition method for producing a single crystal, electronic grade diamond comprising: 20
  - (a) depositing onto a seed plate, comprising a plurality of oriented single crystal diamond seed wafers, a masking layer including a plurality of seed holes spaced apart in the masking layer; 25
  - (b) placing said seed plate into a chemical vapor deposition reactor;
  - (c) growing epitaxial diamond through said seed holes and laterally over the masking layer to form a continuous layer of monocrystalline diamond over said masking layer; 30
  - (d) separating the resulting epitaxially grown diamond layer from said seed plate; and
  - (e) recovering a single crystal, electronic grade diamond having an area greater than the area of each of the individual seed wafers. 35
17. The method of Claim 16 wherein said oriented diamond crystal is used as said seed plate in place of the plurality of oriented single crystal diamond seed wafers. 40
18. The method of Claim 17 wherein the thickness of said epitaxially grown diamond layer is in the range of about 1  $\mu\text{m}$  to about 3000  $\mu\text{m}$ . 45
19. The method of Claim 17 wherein the thickness of said epitaxially grown diamond layer is in the range of about 100 $\mu\text{m}$  to about 2000 $\mu\text{m}$ . 50
20. The method of Claim 16 wherein said epitaxially grown diamond layer is chemical vapor deposited at temperatures in the range of about 250° to about 1100°C. 55
21. The method of Claim 16 wherein said epitaxially grown diamond layer is chemical vapor deposited in a hot filament chemical vapor deposition reactor.
22. The method of Claim 21 wherein said epitaxially grown crystal layer is chemical vapor deposited at temperatures in the range of about 600° to about 1050°C.
23. The method of Claim 16 wherein said epitaxially grown crystal layer is chemical vapor deposited in a microwave-assisted chemical vapor deposition reactor.
24. The method of Claim 16 wherein said epitaxially grown crystal layer is chemical vapor deposited in a plasma-assisted chemical vapor deposition reactor.
25. The method of Claim 16 wherein said epitaxially grown crystal layer is chemical vapor deposited in a radio frequency plasma-assisted chemical vapor deposition reactor.
26. The method of Claim 16 wherein said epitaxially grown crystal layer is chemical vapor deposited in an Electron Cyclotron Resonance plasma-assisted chemical vapor deposition reactor.
27. The method of Claim 16 wherein said epitaxially grown crystal layer is chemical vapor deposited in a direct current plasma-assisted chemical vapor deposition reactor.
28. The method of Claim 16 wherein said epitaxially grown crystal layer is chemical vapor deposited in a direct current arc plasma jet-assisted chemical vapor deposition reactor.
29. The method of Claim 16 wherein said epitaxially grown crystal layer is chemical vapor deposited in an alternating current arc plasma jet-assisted chemical vapor deposition reactor.
30. The method of Claim 16 wherein said epitaxially grown crystal layer is chemical vapor deposited in an electron assisted chemical vapor deposition reactor.
31. The method of Claim 16 wherein said masking layer is selected from the group consisting of silicon, tungsten, molybdenum, nickel, gold, copper, soft carbon, carbon soot, diamond-like carbon, hydrogenated diamond-like carbon, graphite,  $\text{SiO}_2$ ,  $\text{SiC}$ ,  $\text{Si}_3\text{N}_4$ ,  $\text{MgO}$ ,  $\text{CaO}$ , polycarbonate, polypropylene, polyethylene, photores-

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ist compounds.

32. The method of Claim 31 wherein said masking layer is silicon dioxide. 5
33. The method of Claim 16 wherein the epitaxially grown diamond layer is separated from said seed plate by mechanical means. 10
34. The method of Claim 16 wherein the epitaxially grown diamond layer is separated from said seed plate by physical means. 15
35. The method of Claim 34 wherein the epitaxially grown diamond layer is separated from said seed plate by cutting means. 20
36. The method of Claim 35 wherein the epitaxially grown diamond layer is separated from said seed plate by chemical means. 25
37. The method of Claim 36 wherein the epitaxially grown diamond layer is separated from said seed plate by etching a masking layer comprising a carbonaceous material at temperatures in the range of about 250° to about 600° C in the presence of an oxygen-containing atmosphere and raising the temperature in the range of about 600° to about 900° C in an oxygen-containing atmosphere. 30
38. A single crystal, electronic grade freestanding diamond product produced according to Claim 16 wherein said epitaxially grown diamond layer has a high crystalline perfection suitable for electronic applications. 35
39. A single crystal, electronic grade freestanding diamond product produced according to Claim 16 wherein said epitaxially grown diamond layer has a crystal orientation of (100). 40

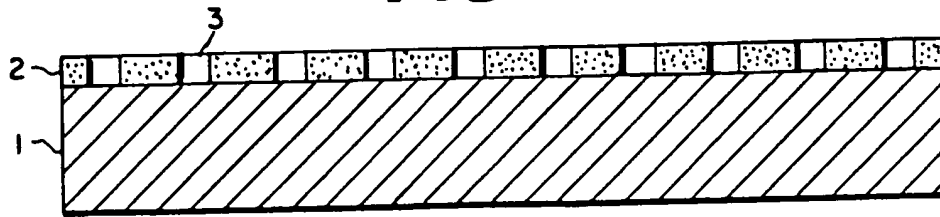
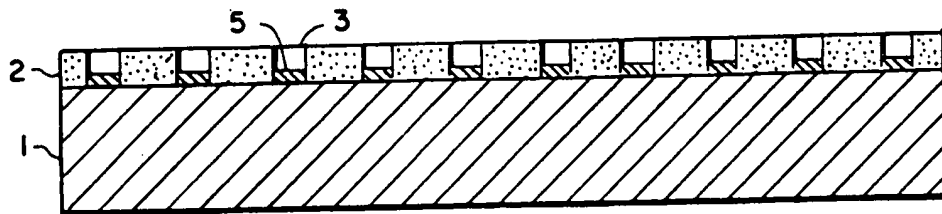
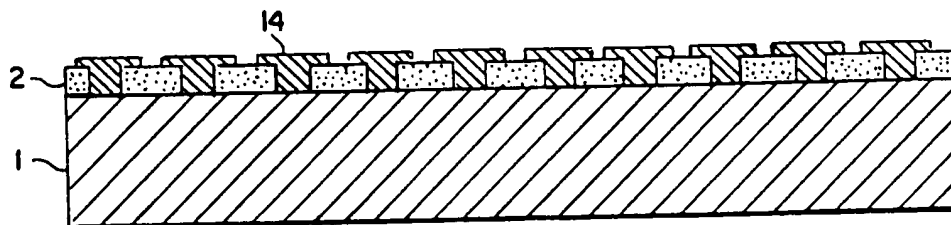
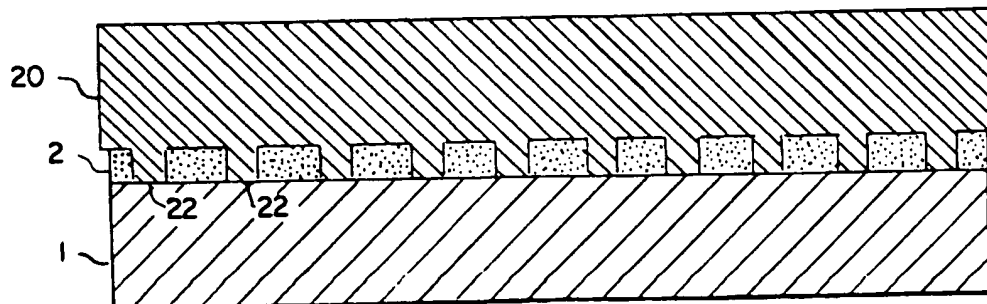
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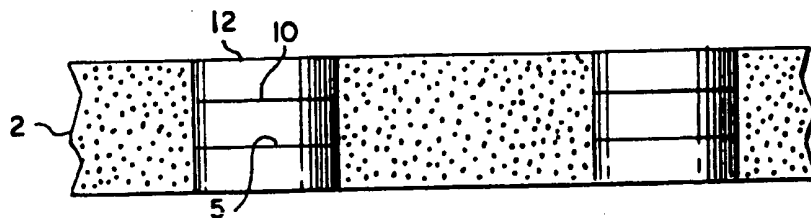
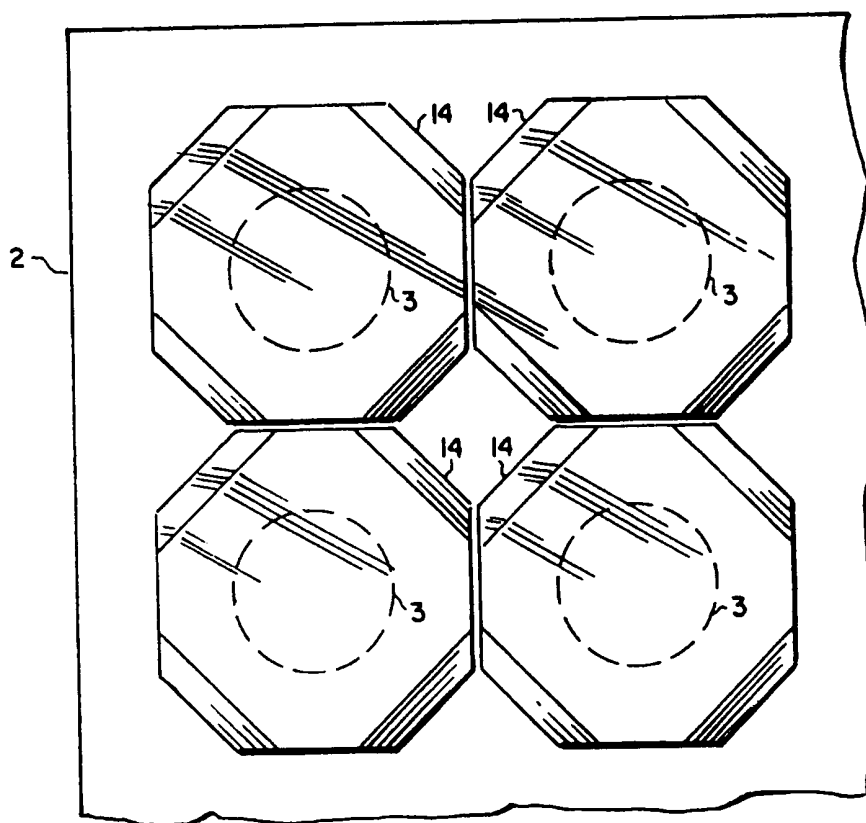
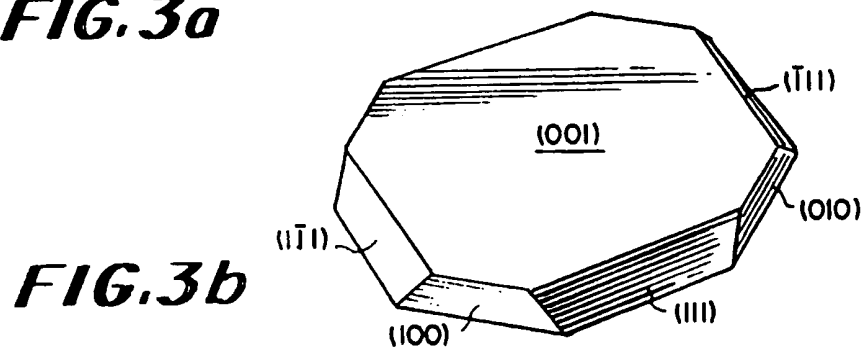
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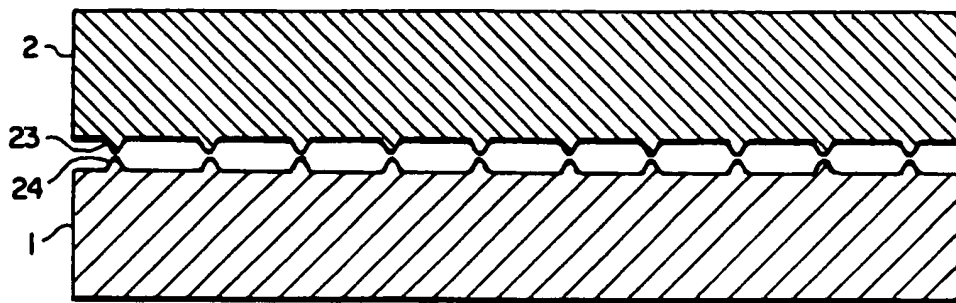
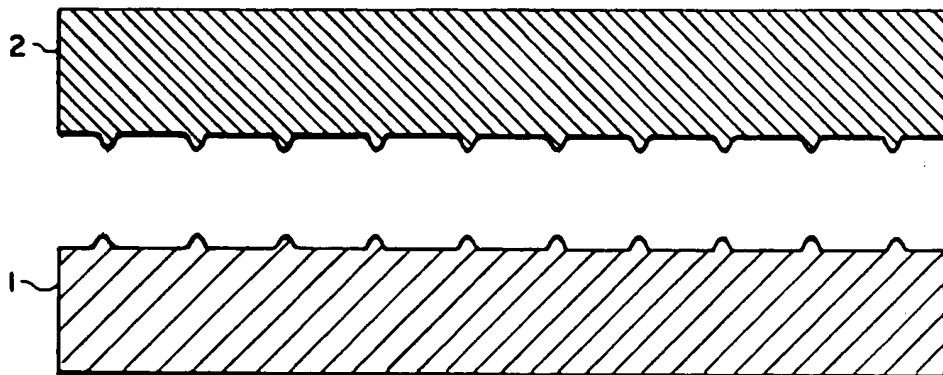
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**FIG. 1****FIG. 2****FIG. 3****FIG. 4**

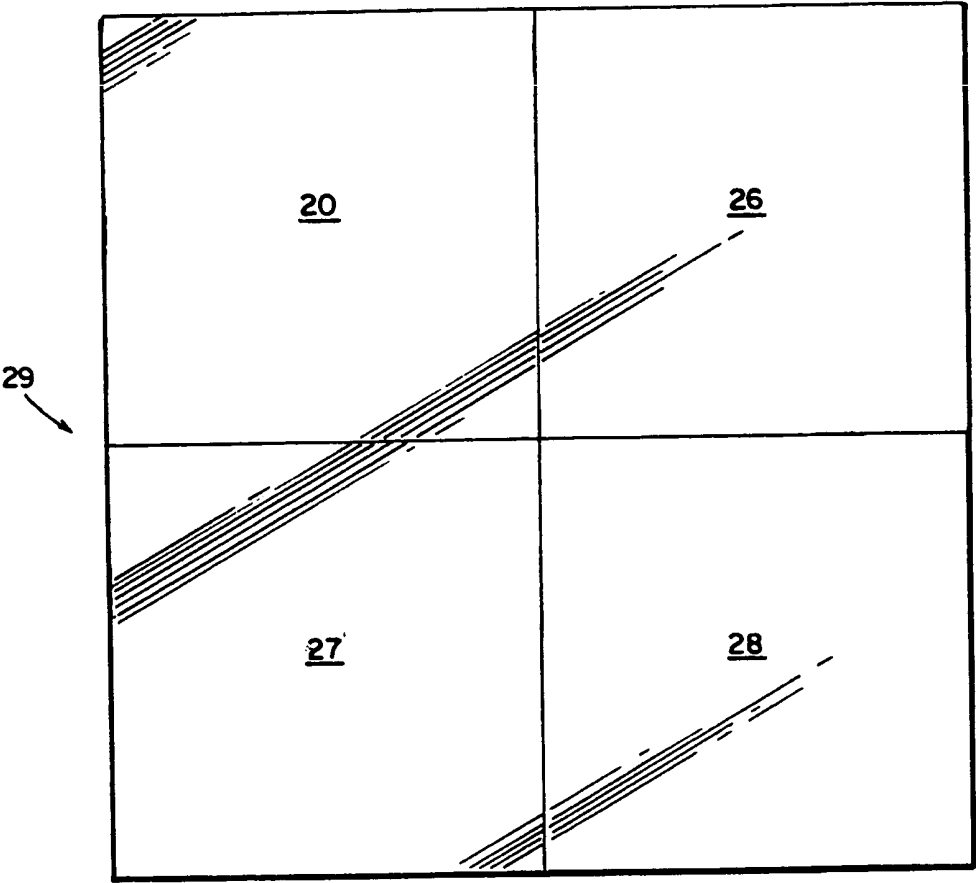
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**FIG. 2a****FIG. 3a****FIG. 3b**

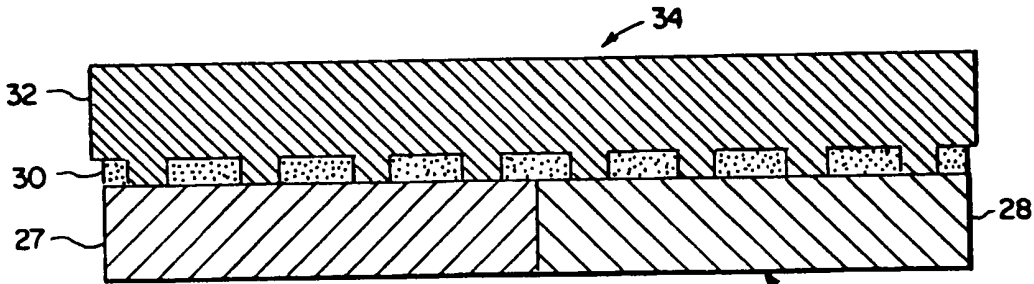
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**FIG. 5****FIG. 6**

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**FIG. 7**



**FIG. 8**





European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number

DOCUMENTS CONSIDERED TO BE RELEVANT			EP 93109130.0
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	<u>US - A - 5 082 522</u> (PURDES et al.) * Claims 1,7,10 *	1,3,6, 12,13, 16,31, 32	C 23 C 16/04 C 30 B 25/04 H 01 L 21/203
A	<u>WO - A - 92/01 092</u> (THE GOVERNMENT OF THE UNITED STATES OF AMERICA) * Claims 1,2,16; examples 1,2 *	1,3, 7-9, 16-19, 38,39	
A	<u>EP - A - 0 399 483</u> (SUMITOMO ELECTRIC INDUSTRIES) * Claims 1,4,5; examples *		
A	<u>US - A - 4 482 422</u> (MCGINN et al.) * Claims 1-4,9 *	1,16	
			<b>TECHNICAL FIELDS SEARCHED (Int. Cl.5)</b>  C 23 C 16/00 C 30 B 25/00 C 30 B 29/00 H 01 L 21/00
The present search report has been drawn up for all claims			
Place of search <b>VIENNA</b>	Date of completion of the search <b>11-08-1993</b>	Examiner <b>PUSTERER</b>	
<b>CATEGORY OF CITED DOCUMENTS</b> X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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